a plurality of memory controllers one of each operatively coupled to each one of the memory, said memory controllers examining a header of a data packet which is forwarded to output ports associated with the crosspoint and examining all bytes said memory control means analyzes all the bytes following said header when said header includes a specific configuration indicating that said packet is a multicast address packet preceding a multicast frame in order to determine whether the packets of said multicast frame are to be forwarded to said output port.

18. The data transmission system of Claim 17 further including at least two adapters operatively coupled to the switch module.

19. The data transmission system of Claim 18 further including at least one LAN operatively coupled to each of the adapters.

REMARKS

This Preliminary Amendment add Claims 17-19 and presents Claims 1-19 for examination.

Respectfully Submitted,

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